#### **Reachability in Timed Counter Systems**

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Analysis

Subclasse

Conclusion

## **Motivation**

#### **Initial observation**



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• need to model time in formal verification ;



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#### we we combine Timed Automata and Counter Systems



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We combine Timed Automata and Counter Systems and we study their reachability matters



- Timed Counter Systems
  - Example
  - Definitions
  - Semantics
- 2 Reachability
  - Counter Reachability Problem
- **3** Analysis of TCS via clock abstraction
  - Region Graph construction
  - The Region Graph as a Counter System

## 4 Subclasses of TCS

- Decidability results
- Algorithm solving the CRP

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# a Timed Counter System $\begin{array}{c} \mathbf{x}_1 < 2 \land \mathbf{x}_2 := 0 & \mathbf{x}_2 > 1 \\ \mathbf{c} := \mathbf{c} + 1 & \mathbf{c} := \mathbf{c} + 1 \\ \hline \begin{pmatrix} \mathbf{e}_1 & \mathbf{x}_1 \geq 2 & \mathbf{e}_3 \\ \mathbf{q}_1 & \mathbf{c} \neq 0 & \mathbf{q}_2 \\ \hline \mathbf{e}_2 & \mathbf{q}_2 \end{array}$

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#### Timed Counter Systems

• Example

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X = a set of *m* real-valued variables, called clocks.  $\mathbf{x} =$  a valuation of the clocks, in  $\mathbb{R}^m_+$ .  $R_X =$  the set of relations on clocks Instructional set of relations is resets and linear guards



$$X$$
 = a set of  $m$  real-valued variables, called clocks. $\mathbf{x}$  = a valuation of the clocks, in  $\mathbb{R}^m_+$ . $R_X$  = the set of relations on clocksImage: usual operations : resets and linear guards

$$C$$
 = a set of *n* integer-valued variables, called counters.  
 $c$  = a valuation of the counters, in  $\mathbb{Z}^n$ .  
 $R_C$  = the set of relations on counters  
 $\square$  Presburger-definable binary relations ( $\equiv$  semi-linear)

Context

(TCS)

# **Definitions (continued)**

#### Definition

A Timed Counter System is a tuple  $\langle Q, X, C, E \rangle$  where :

- Q is a finite set of control states (also called *locations*)
- $E \subseteq Q \times R_X \times R_C \times Q$  is a finite set of transitions (edges)

TCS

# **Definitions (continued)**

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#### Definition

A Timed Automaton is a TCS where  $C = \emptyset$ . A Counter System is a TCS where  $X = \emptyset$ .

#### Timed Counter Systems

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(TCS)

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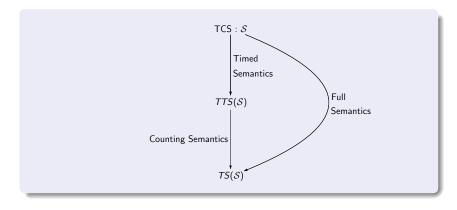
## The different semantics of a TCS $\ensuremath{\mathcal{S}}$

- Counting Transition System CTS(S)
- Timed Transition System TTS(S)
- full Transition System TS(S)

TCS

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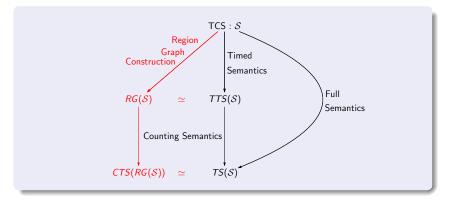


(TCS)

## The different semantics of a TCS $\ensuremath{\mathcal{S}}$

- Counting Transition System CTS(S)
- Timed Transition System  $TTS(S) \simeq \text{Region Graph } RG(S)$
- full Transition System TS(S)

 $\simeq \text{Region Graph } RG(\mathcal{S}) \\ \simeq CTS(RG(\mathcal{S}))$ 



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Clocks are used for modelling temporal requirements ; their *exact* value does not really matter.

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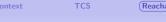


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Counter Reachability Problem (CRP)

**Inputs** : A TCS S, an initial configuration  $s_0$  of TS(S), and a configuration  $(q, \mathbf{c})$  of CTS(S). **Question** : Is there a clock valuation  $\mathbf{x}$  such that  $(q, \mathbf{x}, \mathbf{c})$  is reachable from  $s_0$  in TS(S)?

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Analysis

## Reachability

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The CRP extends the classical reachability problem of CS, known to be undecidable ; therefore CRP is undecidable for TCS.



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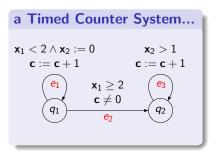
TCS



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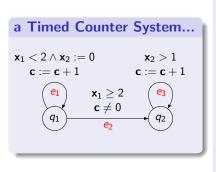
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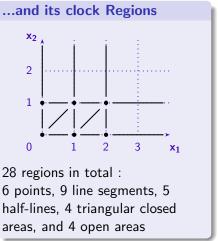
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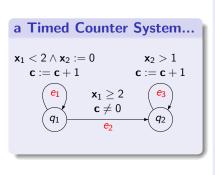
## **Example**

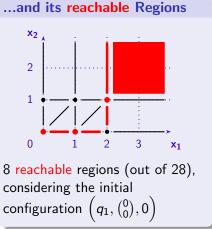






## Example



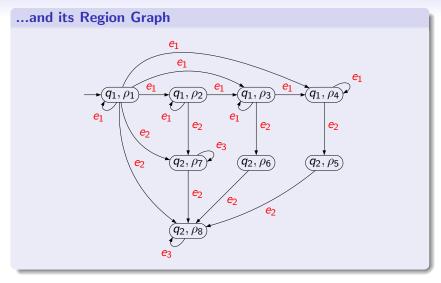


Context

TCS



## Example (continued)



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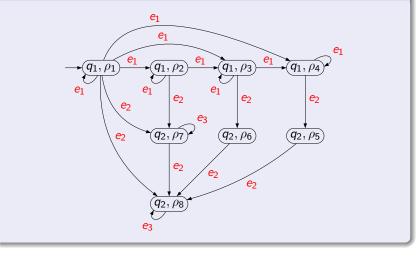
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Context



# Example (continued)

...and its Region Graph which is a Counter System !



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#### Key idea :

For a TCS S, its region graph RG(S) is also a Counter System (namely because it has a finite number of states).



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Let  $\mathfrak{C}$  be a class of TCS such that there is an algorithm solving the classical reachability problem for RG(S), for any  $S \in \mathfrak{C}$ .

#### Theorem

The Counter Reachability Problem is decidable for  $\mathfrak{C}$ .



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#### Theorem

The Counter Reachability Problem is decidable for  $\mathfrak{C}$ .

Proof idea • time-abstract bisimulation

By definition, CTS(TTS(S)) = TTS(CTS(S)) = TS(S). It is well-known that  $RG(S) \simeq TTS(S)$ . Therefore  $CTS(RG(S)) \simeq TS(S)$ .



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### Subclasses of TCS

- Decidability results
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# Subclasses of TCS

- Timed Counter Machine (TCM) = TCS whose relations on counters are translations with guards of the form b ≤ c or b = c, where b ∈ N<sup>n</sup>
- Timed VASS (TVASS) = TCM without b = c guards
- Bounded TCS = TCS whose counter values are bounded
- Reversal-Bounded TCM = TCM whose counters do a bounded number of alternations between increasing and decreasing modes

# Subclasses of TCS

- Timed Counter Machine (TCM) = TCS whose relations on counters are translations with guards of the form  $b < \mathbf{c}$  or  $b = \mathbf{c}$ , where  $b \in \mathbb{N}^n$
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Deci	dability results		
	Model	Region Graph	Counter Reachability
	TCS	CS	Undecidable
	TVASS	VASS	Decidable
	Reversal-bounded TCM	Reversal-bounded CM	Decidable
	Bounded TCS	Bounded CS	Decidable

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# Algorithm solving the CRP

Since TVASS is a recursive class, we propose an algorithm solving the CRP for this class :

**Inputs** : A TVASS S, a configuration  $(q, \mathbf{c})$ , and an initial state  $s_0$ **Output** : Answers "Is there a **x** such that  $(q, \mathbf{x}, \mathbf{c})$  is reachable from  $s_0$  in TS(S)?"

- **1** Build RG(S)
- ② For all state  $(q', [\mathbf{x}])$  of RG(S) do

If 
$$q' = q$$
 then

- If ((q, [x]), c) is reachable in RG(S) from  $s_0$  then
- seturn True

return False

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#### Contribution

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#### Contribution

• Introduction of a new model mixing clocks and counters (TCS)



### Conclusion

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- Variation of the classical reachability problem (CRP)



### Conclusion

#### Contribution

- Introduction of a new model mixing clocks and counters (TCS)
- Variation of the classical reachability problem (CRP)
- Decidability results for CRP on 3 subclasses of TCS



#### **Future work**

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#### **Future work**

• Broaden decidability results : flat TCS, etc...



## Conclusion

#### **Future work**

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- Extend the tool FAST [BFLP03] with time

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Subclasse



### Conclusion

#### **Future work**

- Broaden decidability results : flat TCS, etc...
- Extend the tool FAST [BFLP03] with time
- Generalize our main theorem to other datatypes than counters : pushdown stacks, lossy channels, etc...

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Reachability

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### **Related work**

Systems related to our Timed Counter Systems :

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• Hybrid Automata [ACHH92]

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- real-valued counters [DIPX04, XDIP03]

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