

Model Checking Branching Time Logics

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Branching-time logics are temporal logics that allow quantification over possible futures [3, 4]. Such logics have been considered very early by the automated verification community because efficient model-checking algorithms for logics like *CTL* could be easily implemented and were used successfully [1].

When L is a (pure-future) linear-time logic, one obtains a branching-time logic $B(L)$ by extending L with quantification over branches. Thus CTL^* is $B(LTL)$ and CTL is $B(LTL_1)$, where we let LTL_1 denote the subset of LTL formulae having at most one occurrence of a temporal modality. Using a model-checking algorithm for L as an oracle, one directly obtains a model-checking algorithm for $B(L)$. Hence, if the model-checking problem for L has complexity C , the model-checking problem for $B(L)$ is in P^C [5, 9].

These observations are particularly illuminating for branching-time logics $B(L)$ where L has a NP-complete model-checking problem [2]. This is the case for some well-known logics, like CTL^+ or $FCTL$ [6], but also for some more expressive logics like BTL_2 [8] or $B^*(X)$ [10]. In such cases, the model-checking problem for $B(L)$ is in P^{NP} , i.e., the level Δ_2^P of the polynomial-time hierarchy, and it is of course both NP-hard and coNP-hard.

For these logics, the problem of closing the gap between these upper and lower bounds was left open until recent years and required the development of new techniques. In one direction, lower bounds tighter than NP- and coNP-hardness have been proved with new reductions from nested satisfiability problems [6, 10, 7]. In the other direction, new algorithms have been developed to solve model-checking problems with a limited number of invocations of the NP oracle [10].

References

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