

Temporal Logics for Concurrent Recursive Programs: Satisfiability and Model Checking*

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Abstract. We develop a general framework for the design of temporal logics for concurrent recursive programs. A program execution is modeled as a partial order with multiple nesting relations. To specify properties of executions, we consider any temporal logic whose modalities are definable in monadic second-order logic and that, in addition, allows PDL-like path expressions. This captures, in a unifying framework, a wide range of logics defined for ranked and unranked trees, nested words, and Mazurkiewicz traces that have been studied separately. We show that satisfiability and model checking are decidable in EXPTIME and 2EXPTIME, depending on the precise path modalities.

1 Introduction

We are concerned with the analysis of computer programs and systems that consist of several components sharing an access to resources such as variables or channels. Any component itself might be built of several modules that can be called recursively resulting in complex infinite-state systems. The analysis of such programs, which consist of a fixed number of recursive threads communicating with one another, is particularly challenging, due to the intrinsically high complexity of interaction between its components. All the more, it is important to provide tools and algorithms that support the design of correct programs, or verify if a given program corresponds to a specification.

It is widely acknowledged that linear-time temporal logic (LTL) [20] is a yardstick among the specification languages. It combines high expressiveness (equivalence to first-order logic [15]) with a reasonable complexity of decision problems such as satisfiability and model checking. LTL has originally been considered for finite-state sequential programs. As real programs are often concurrent or rely on recursive procedures, LTL has been extended in two directions.

First, asynchronous finite-state programs (asynchronous automata) [24] are a formal model of shared-memory systems and properly generalize finite-state sequential programs. Their executions are no longer sequential (i.e., totally ordered) but can be naturally modeled as graphs or partial orders. In the literature, these structures are known as *Mazurkiewicz traces*. They look back on a long list

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of now classic results that smoothly extend the purely sequential setting (e.g., expressive equivalence to first-order logic) [10,9].

Second, in an influential paper, Alur and Madhusudan extend the finite-state sequential model to *visibly pushdown automata* (VPA) [3]. VPA are a flexible model for recursive programs, where subroutines can be called and executed while the current thread is suspended. The execution of a VPA is still totally ordered. However, it comes with some extra information that relates a subroutine call with the corresponding return position, which gives rise to the notion of *nested words* [3]. Alur et al. recently defined versions of LTL towards this infinite-state setting [2,1] that can be considered as canonical counterparts of the classical logic introduced by Pnueli.

To model programs that involve both recursion and concurrency, one needs to mix both views. Most approaches to modeling concurrent recursive programs, however, reduce concurrency to interleaving and neglect a behavioral semantics that preserves independencies between program events [21,16,17,4]. A first model for concurrent recursive programs with partial-order semantics was considered in [5]. Executions of their *concurrent VPA* equip Mazurkiewicz traces with multiple nesting relations. Temporal logics have not been considered, though, and there is for now no canonical merge of the two existing approaches. It must be noted that satisfiability is undecidable when considering multiple nesting relations, even for simple logics. Yet, it becomes decidable if we restrict to system behaviors that can be executed within a bounded number of phase switches, a notion introduced in [16]. A phase switch consists of a transfer of control from one process to another. This allows for the discovery of many errors, since they typically manifest themselves after a few phase switches [21].

In this paper, we present linear-time temporal logics for concurrent recursive programs. A temporal logic is parametrized by a finite set of modalities that are definable in monadic second-order logic (cf. [12]). In addition, it provides path expressions similar to those from PDL [11] or XPath [19], which are orthogonal to the modalities. This general framework captures temporal logics considered in [2,1,8] when we restrict to one process, and it captures those considered in [9,12,13] when we go without recursion. Our decision procedures for the (bounded phase) satisfiability problem are optimal in all these special cases, but provide a unifying proof. They also apply to other structures such as ranked and unranked trees. We then use our logics for model checking. To do so, we provide a system model whose behavioral semantics preserves concurrency (unlike the models from [21,4,16]). The complexity upper bounds from satisfiability are preserved.

Summarizing, we provide a first framework to specify linear-time properties of concurrent recursive programs appropriately over partial orders.

Outline In Section 2, we introduce some basic notions such as graphs and trees, and we define nested traces, which serve as our model of program executions. Section 3 provides a range of temporal logics over nested traces. In Section 4, we state and solve their satisfiability problem. Section 5 addresses model checking.

2 Graphs, Nested Traces, and Trees

To model the behavior of distributed systems, we consider labeled graphs, each representing one single execution. A node of a graph is an event that can be observed during an execution. Its labeling reveals its type (e.g., procedure call, return, or internal) or some processes that are involved in its execution. Edges reflect causal dependencies: an edge (u, v) from node u to node v implies that u happens before v . A labeling of (u, v) may provide information about the kind of causality between u and v (e.g., successive events on some process).

Accordingly, we consider a *signature*, which is a pair $\mathcal{S} = (\Sigma, \Gamma)$ consisting of a finite set Σ of node labelings and a finite set Γ of edge labelings. Throughout the paper, we assume $|\Sigma| \geq 1$ and $|\Gamma| \geq 2$. An \mathcal{S} -*graph* is a structure $G = (V, \lambda, \nu)$ where V is a non-empty set of countably many *nodes*, $\lambda : V \rightarrow 2^\Sigma$ is the *node-labeling function*, and $\nu : (V \times V) \rightarrow 2^\Gamma$ is the *edge-labeling function*, with the intuitive understanding that there is an edge between u and v iff $\nu(u, v) \neq \emptyset$. For $\sigma \in \Sigma$, $V_\sigma := \{u \in V \mid \sigma \in \lambda(u)\}$ denotes the set of nodes that are labeled with σ . Moreover, for $\gamma \in \Gamma$, $E_\gamma := \{(u, v) \in V \times V \mid \gamma \in \nu(u, v)\}$ denotes the set of edges with labeling γ . Then, $E := \bigcup_{\gamma \in \Gamma} E_\gamma$ is the set of all the edges. We require that the transitive closure E^+ of E is a well-founded (strict) partial order on V . We write \prec^G or simply \prec for E^+ , and we write \preceq^G or \preceq for E^* . Next, we consider concrete classes of \mathcal{S} -graphs.

Nested Traces To model executions of concurrent recursive programs that communicate via shared variables, we introduce graphs with multiple nesting relations. We fix non-empty finite sets $Proc$ and Act , and let $Type = \{call, ret, int\}$. Then, $\Sigma = Proc \cup Act \cup Type$ is the set of node labelings. Its component $Type$ indicates whether an event is a procedure *call*, a *return*, or an *internal* action. A nesting edge connects a procedure call with the corresponding return, and will be labeled by $cr \in \Gamma$. In addition, we use $succ_p \in \Gamma$ to label those edges that link successive events of process $p \in Proc$. Thus, $\Gamma = \{succ_p \mid p \in Proc\} \cup \{cr\}$. We obtain the signature $\mathcal{S} = (\Sigma, \Gamma)$. Formally, a *nested (Mazurkiewicz) trace* over $Proc$ and Act is an \mathcal{S} -graph $G = (V, \lambda, \nu)$ such that the following hold:

- T1 $V = V_{call} \uplus V_{ret} \uplus V_{int} = \biguplus_{a \in Act} V_a = \bigcup_{p \in Proc} V_p$
- T2 for all processes $p, q \in Proc$ with $p \neq q$, we have $V_p \cap V_q \subseteq V_{int}$
- T3 for all $p \in Proc$, E_{succ_p} is the direct successor relation of a total order on V_p
- T4 $E_{cr} \subseteq (V_{call} \times V_{ret}) \cap \bigcup_{p \in Proc} (V_p \times V_p)$
- T5 for all $(u, v), (u', v') \in E_{cr}$, we have $u = u'$ iff $v = v'$
- T6 for all $p \in Proc$ and $u \in V_{call} \cap V_p$ and $v' \in V_{ret} \cap V_p$, if $u \prec v'$ then either there exists $v \preceq v'$ with $(u, v) \in E_{cr}$ or there exists $u' \succeq u$ with $(u', v') \in E_{cr}$

Intuitively, each event has exactly one type and one action and belongs to at least one process (T1), synchronizing events are always internal (T2), along any process the events are totally ordered (T3), a nesting edge is always between a call and a return of the same process (T4), and cr -edges restricted to any process are well nested (T5 and T6). Note that we may have unmatched calls or returns.

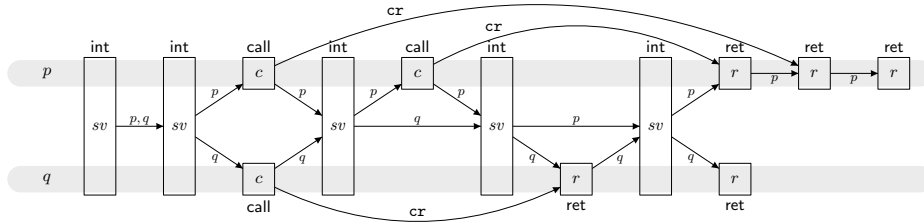


Fig. 1. A nested trace over $Proc = \{p, q\}$ and $Act = \{c, r, sv\}$

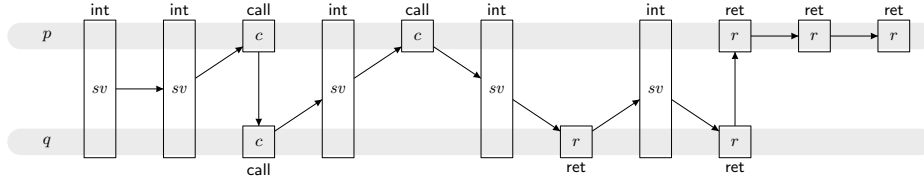


Fig. 2. A 2-phase linearization

For $u \in V$, we let $Proc(u) = \lambda(u) \cap Proc$. When $|Proc| = 1$, then a nested trace is a nested word in the classical sense [3]. The set of nested traces over $Proc$ and Act is denoted by $Traces(Proc, Act)$. Figure 1 depicts a nested trace over $Proc = \{p, q\}$ and $Act = \{c, r, sv\}$. Action c denotes a call, r a return, and sv reveals some synchronization via a shared variable. Node labelings from $Proc$ are given by the gray-shaded regions, i.e., sv -events involve both p and q . Edge labelings $succ_p$ and $succ_q$ are abbreviated by p and q , resp.

We introduce a restricted class of nested traces over $Proc$ and Act . It is parametrized by an (existential) upper bound $k \geq 1$ on the number of *phases* that a trace needs to be executed. In each phase, return events belong to one dedicated process. Let us first introduce the notion of *linearization*. A linearization of a nested trace $G = (V, \lambda, \nu)$ is any structure (V, λ, \leq) such that \leq is a total order extending \preceq . Fig. 2 depicts a linearization of the nested trace from Fig. 1. We identify isomorphic structures so that a linearization can be considered as a word over 2^Σ . Note that, for every word $w \in (2^\Sigma)^*$, there is at most one (up to isomorphism) nested trace G such that w is a linearization of G [10].

For $k \geq 1$, a word $w \in (2^\Sigma)^*$ is a *k-phase word* if it can be written as $w_1 \cdots w_k$ where, for all $i \in \{1, \dots, k\}$, there is $p \in Proc$ such that for each letter a of w_i we have $ret \in a$ implies $p \in a$. A nested trace is called a *k-phase nested trace* if at least one of its linearizations is a *k-phase word*. The set of *k-phase nested traces* over $Proc$ and Act is denoted by $Traces_k(Proc, Act)$. We denote by $Lin_k(G)$ the set of linearizations of nested trace G that are *k-phase words*. In particular, G is a *k-phase nested trace* iff $Lin_k(G) \neq \emptyset$. The nested trace from Fig. 1 is a 2-phase trace: its linearization from Fig. 2 schedules returns of q before all returns of p .

Ranked Trees Let $\mathcal{S} = (\Sigma, \Gamma)$. An \mathcal{S} -tree is an \mathcal{S} -graph $t = (V, \lambda, \nu)$. We require that there is a “root” $u_0 \in V$ such that for all $u, v, v' \in V$ and $\gamma, \gamma' \in \Gamma$:

- (i) $(u_0, u) \in E^*$, and $(v, u), (v', u) \in E$ implies $v = v'$

- (ii) $(u, v), (u, v') \in E_\gamma$ implies $v = v'$, and $(u, v) \in E_\gamma \cap E_{\gamma'}$ implies $\gamma = \gamma'$

The tree structure is enforced by (i), and (ii) ensures that every node has at most one γ -successor and edges have a unique label from Γ , which can be seen as a set of *directions*. Thus, $\Gamma = \{\mathbf{left}, \mathbf{right}\}$ yields binary trees. The set of all \mathcal{S} -trees is denoted $Trees(\mathcal{S})$.

Ordered Unranked Trees Each node in an ordered unranked tree can have a potentially unbounded number of children, and the children of any node are totally ordered. Formally it is an \mathcal{S} -graph $t = (V, \lambda, \nu)$ over $\mathcal{S} = (\Sigma, \Gamma)$ where $\Gamma = \{\mathbf{child}, \mathbf{next}\}$. Again, there is a “root” $u_0 \in V$ such that for all $u, v, v' \in V$:

- (i) $(u_0, u) \in E^*$ and $(u_0, u) \notin E_{\mathbf{next}}$
(ii) $(v, u), (v', u) \in E_{\mathbf{child}}$ implies $v = v'$, and $(v, u), (v', u) \in E_{\mathbf{next}}$ implies $v = v'$
(iii) $(u, v), (u, v') \in E_{\mathbf{next}}$ implies $v = v'$ and $(u, v) \in E_\gamma \cap E_{\gamma'}$ implies $\gamma = \gamma'$
(iv) $(u, v) \in E_{\mathbf{child}}$ implies that there exists $v_0 \in V$ such that $(u, v_0) \in E_{\mathbf{child}}$ and, $(u, v') \in E_{\mathbf{child}}$ if and only if $(v_0, v') \in E_{\mathbf{next}}^*$.

The set of all ordered unranked trees over \mathcal{S} is denoted $o.u. Trees(\mathcal{S})$.

3 Temporal Logic

In this section, let $\mathcal{S} = (\Sigma, \Gamma)$ be any signature. We study temporal logics whose modalities are defined in the monadic second-order (MSO) logic over \mathcal{S} -graphs, which we recall in the following. We use x, y, \dots to denote first-order variables which vary over nodes of the graphs, and X, Y, \dots to denote second-order variables which vary over sets of nodes. The syntax of $MSO(\mathcal{S})$ is given by the grammar $\varphi ::= \sigma(x) \mid \gamma(x, y) \mid x = y \mid x \in X \mid \neg\varphi \mid \varphi \vee \varphi \mid \exists x\varphi \mid \exists X\varphi$ where σ ranges over Σ , γ ranges over Γ , x and y are first-order variables, and X is a second-order variable. We use \prec , the transitive closure of the relations induced by Γ , freely as it can be expressed in $MSO(\mathcal{S})$. For an \mathcal{S} -graph $G = (V, \lambda, \nu)$ and a formula $\varphi(x_1, \dots, x_n, X_1, \dots, X_m)$ with free variables in $\{x_1, \dots, x_n, X_1, \dots, X_m\}$, we write $G \models \varphi(u_1, \dots, u_n, U_1, \dots, U_m)$ if φ is evaluated to true when interpreting the variables by $u_1, \dots, u_n \in V$ and $U_1, \dots, U_m \subseteq V$, respectively.

We will use MSO formulas to define modalities of a temporal logic. For $m \in \mathbb{N} = \{0, 1, 2, \dots\}$, we call $\varphi \in MSO(\mathcal{S})$ an *m-ary modality* if its free variables consist of m set variables X_1, \dots, X_m and one first-order variable x .

A *temporal logic* over \mathcal{S} is given by a triple $\mathcal{L} = (\mathcal{M}, \mathit{arity}, \llbracket - \rrbracket)$ including a finite set \mathcal{M} of *modality names*, a mapping $\mathit{arity} : \mathcal{M} \rightarrow \mathbb{N}$, and a mapping $\llbracket - \rrbracket : \mathcal{M} \rightarrow MSO(\mathcal{S})$ such that, for $M \in \mathcal{M}$ with $\mathit{arity}(M) = m$, $\llbracket M \rrbracket$ is an m -ary modality. Its syntax, i.e., the set of *formulas* $\varphi \in \text{Form}(\mathcal{L})$ is given by

$$\begin{aligned} \varphi &::= \sigma \mid \neg\varphi \mid \varphi \vee \varphi \mid \underbrace{M(\varphi, \dots, \varphi)}_{\mathit{arity}(M)} \mid \exists\pi \\ \pi &::= ?\varphi \mid \gamma \mid \gamma^{-1} \mid \pi \cup \pi \mid \pi \cap \pi \mid \pi \circ \pi \mid \pi^* \end{aligned}$$

$$\begin{aligned}
\llbracket \sigma \rrbracket_G &:= V_\sigma & \llbracket \neg \varphi \rrbracket_G &:= V \setminus \llbracket \varphi \rrbracket_G & \llbracket \varphi_1 \vee \varphi_2 \rrbracket_G &:= \llbracket \varphi_1 \rrbracket_G \cup \llbracket \varphi_2 \rrbracket_G \\
\llbracket M(\varphi_1, \dots, \varphi_m) \rrbracket_G &:= \{u \in V \mid G \models \llbracket M \rrbracket(u, \llbracket \varphi_1 \rrbracket_G, \dots, \llbracket \varphi_m \rrbracket_G)\} \\
\llbracket \exists \pi \rrbracket_G &:= \{u \in V \mid \text{there is } v \in V \text{ such that } (u, v) \in \llbracket \pi \rrbracket_G\} \\
\llbracket ?\varphi \rrbracket_G &:= \{(u, u) \mid u \in \llbracket \varphi \rrbracket_G\} & \llbracket \gamma \rrbracket_G &:= E_\gamma & \llbracket \gamma^{-1} \rrbracket_G &:= E_\gamma^{-1} \\
\llbracket \pi \otimes \tau \rrbracket_G &:= \llbracket \pi \rrbracket_G \otimes \llbracket \tau \rrbracket_G & \llbracket \pi^* \rrbracket_G &:= \llbracket \pi \rrbracket_G^*
\end{aligned}$$

Fig. 3. Semantics of temporal logic

where σ ranges over Σ , M ranges over \mathcal{M} , and γ ranges over Γ . We call φ a *node formula* and π a *path formula* (or *path expression*). Their semantics wrt. an \mathcal{S} -graph $G = (V, \lambda, \nu)$ is defined inductively: for subformulas φ , we obtain a set $\llbracket \varphi \rrbracket_G \subseteq V$, containing the nodes of G that satisfy φ . Accordingly, $\llbracket \pi \rrbracket_G \subseteq V \times V$ is the set of pairs of nodes linked with a path defined by π . Then, $\exists \pi$ is the set of nodes that admit a path following π . Formally, $\llbracket - \rrbracket_G$ is given in Fig. 3 where $\otimes \in \{\cup, \cap, \circ\}$ (\circ denotes the product of two relations). We may write $G, u \models \varphi$ if $u \in \llbracket \varphi \rrbracket_G$ and $G, u, v \models \pi$ if $(u, v) \in \llbracket \pi \rrbracket_G$. We also use $\pi^+ := \pi \circ \pi^*$.

An *intersection free temporal logic* over \mathcal{S} is defined as expected: path expressions do not contain subformulas of the form $\pi_1 \cap \pi_2$. Moreover, a *path-expression free temporal logic* does not contain formulas of the form $\exists \pi$.

Remark 1. We can easily include π^{-1} also in our syntax but it is redundant: $(?\varphi)^{-1} = ?\varphi$, $(\pi^{-1})^{-1} = \pi$, $(\pi_1 \cup \pi_2)^{-1} = \pi_1^{-1} \cup \pi_2^{-1}$, $(\pi_1 \cap \pi_2)^{-1} = \pi_1^{-1} \cap \pi_2^{-1}$, $(\pi_1 \circ \pi_2)^{-1} = \pi_2^{-1} \circ \pi_1^{-1}$ and $(\pi^*)^{-1} = (\pi^{-1})^*$.

Example 2. We consider the path-expression free temporal logic CTL over (Σ, Γ) (interpreted over (Σ, Γ) -trees) [7]. The modalities are $\mathcal{M} = \{\text{EX}, \text{EG}, \text{EU}\}$ with EX and EG being unary and EU being binary. Node formula EX φ holds at a node if there is a child satisfying φ . Thus, $\llbracket \text{EX} \rrbracket(x, X) = \exists y (x \prec y \wedge y \in X)$ where $x \prec y := \bigvee_{\gamma \in \Gamma} \gamma(x, y)$. Formula EG φ means that there is an infinite path starting from the current node where φ always holds. Formula φ EU ψ means that there is a path starting from the current node satisfying φ until ψ :

$$\begin{aligned}
\llbracket \text{EG} \rrbracket(x, X) &= \exists Y (Y \subseteq X \wedge x \in Y \wedge \forall z (z \in Y \rightarrow \exists z' (z' \in Y \wedge z \prec z'))) \\
\llbracket \text{EU} \rrbracket(x, X_1, X_2) &= \exists z (x \preceq z \wedge z \in X_2 \wedge \forall y (x \preceq y \prec z \rightarrow y \in X_1))
\end{aligned}$$

Example 3. Our approach captures various logics over unranked trees (see [19] for an overview). E.g., the intersection free temporal logic \mathcal{L}_0^- with no modalities over ordered unranked trees is precisely regular XPath [6].

Example 4. We give a property over nested traces using a path expression: $\varphi = \neg \exists (\text{cr} \cap (?q \circ (\bigcup_{\gamma \in \Gamma} \gamma)^+ \circ ?(\text{call} \wedge p) \circ (\bigcup_{\gamma \in \Gamma} \gamma)^+))$ means that process p is not allowed to call a new procedure when it is in the scope of an active procedure call from q . The first call node along q in Fig. 1 does not satisfy this property due to the second call of p .

Example 5. We now present a path-expression free temporal logic over nested traces, $\text{NTrLTL} = (\mathcal{M}, \text{arity}, \llbracket - \rrbracket)$. The unary modalities are given by $\{\mathbf{X}^{\text{cr}}, \mathbf{Y}^{\text{cr}}\} \cup \{\mathbf{X}_p, \mathbf{Y}_p \mid p \in \text{Proc}\}$. Intuitively, $\mathbf{X}_p \varphi$ means that φ holds at the next p -position and \mathbf{X}^{cr} claims that we are at a call position and φ holds at the corresponding return position. The dual *past* modalities are \mathbf{Y}_p and \mathbf{Y}^{cr} . Formally, the semantics of the *future* modalities is given by

$$\begin{aligned} \llbracket \mathbf{X}_p \rrbracket(x, X) &= \exists y (p(y) \wedge x \prec y \wedge y \in X \wedge \forall z (p(z) \wedge x \prec z \rightarrow y \preceq z)) \\ \llbracket \mathbf{X}^{\text{cr}} \rrbracket(x, X) &= \exists y (\text{cr}(x, y) \wedge y \in X) \end{aligned}$$

The binary modalities consist of $\{\text{EU}, \text{ES}, \text{AU}, \text{AS}, \text{EU}^a, \text{ES}^a, \text{EU}^s, \text{ES}^s\}$ and $\{\mathbf{U}_p, \mathbf{S}_p, \mathbf{U}_p^a, \mathbf{S}_p^a, \mathbf{U}_p^s, \mathbf{S}_p^s \mid p \in \text{Proc}\}$. Formula $\varphi \text{EU} \psi$ means that in the *graph* G there is a path not using **cr**-edges from the current node satisfying φ until ψ , whereas $\varphi \text{AU} \psi$ means that in the *partial order* G there is a future node satisfying ψ , and φ should hold on all nodes in between. Formally, the semantics is given by

$$\begin{aligned} \llbracket \text{AU} \rrbracket(x, X_1, X_2) &= \exists z (x \preceq z \wedge z \in X_2 \wedge \forall y (x \preceq y \prec z \rightarrow z \in X_1)) \\ \llbracket \text{EU} \rrbracket(x, X_1, X_2) &= \exists z \exists Y (z \in X_2 \wedge Y \subseteq X_1 \wedge \forall y (y \in Y \vee y = z) \rightarrow (y = x \vee \\ &\quad \exists y' (y' \in Y \wedge \bigvee_{q \in \text{Proc}} \text{succ}_q(y', y)))) \end{aligned}$$

A *summary* path in G is a path that may freely use **cr**-edges. Formally, the semantics $\llbracket \text{EU}^s \rrbracket(x, X_1, X_2)$ is defined as

$$\begin{aligned} \exists z \exists Y (z \in X_2 \wedge Y \subseteq X_1 \wedge \forall y (y \in Y \vee y = z) \rightarrow (y = x \vee \\ \exists y' (y' \in Y \wedge (\text{cr}(y', y) \vee \bigvee_{q \in \text{Proc}} \text{succ}_q(y', y)))))) \end{aligned}$$

An *abstract* path in G is a path which does not take a succ_q -edge *from* a call node or *to* a return node. Restricting to abstract paths we obtain the modality EU^a whose semantics $\llbracket \text{EU}^a \rrbracket(x, X_1, X_2)$ is defined as

$$\begin{aligned} \exists z \exists Y (z \in X_2 \wedge Y \subseteq X_1 \wedge \forall y (y \in Y \vee y = z) \rightarrow (y = x \vee \\ \exists y' (y' \in Y \wedge (\text{cr}(y', y) \vee (\neg \text{call}(y') \wedge \neg \text{ret}(y) \wedge \bigvee_{q \in \text{Proc}} \text{succ}_q(y', y)))))) \end{aligned}$$

Modalities \mathbf{U}_p , \mathbf{U}_p^s and \mathbf{U}_p^a are obtained from EU , EU^s and EU^a by restricting to paths on process p . The semantics is obtained easily by adding the constraint $p(x)$ and replacing $\bigvee_{q \in \text{Proc}} \text{succ}_q$ by succ_p in the formulas above. For instance, the formula $\top \mathbf{U}_p^a q$ says that in the abstract future (that is, in the scope of the current procedure call and with the same stack contents) of process p , there is a synchronizing action with process q .

Modalities AS , ES , ES^s , ES^a , \mathbf{S}_p , \mathbf{S}_p^s , and \mathbf{S}_p^a are the past-time counterparts of the above future modalities.

Note that this example captures various logics defined in [1] for the case $|\text{Proc}| = 1$. For example, when we choose $\{\mathbf{X}^{\text{cr}}, \mathbf{Y}^{\text{cr}}, \mathbf{X}_p, \mathbf{Y}_p, \text{EU}^s, \text{ES}^s\}$ to be our modalities (where p is the only process in Proc), our logic is precisely NWTL.

In the next section we show that satisfiability problem of any MSO-definable temporal logic \mathcal{L} over any of the structures defined in Section 2 (that is, k -phase nested traces, ranked trees, and unranked trees) is decidable in 2EXPTIME. Moreover, if \mathcal{L} is intersection free, then it is decidable in EXPTIME.

4 Satisfiability: From Trees to Nested Traces

Consider any signature $\mathcal{S} = (\Sigma, \Gamma)$ and temporal logic \mathcal{L} over \mathcal{S} . The following decision problem is well known.

Problem 6. TREE-SAT(\mathcal{L}):

INSTANCE: $\varphi \in \text{Form}(\mathcal{L})$

QUESTION: Are there $t \in \text{Trees}(\mathcal{S})$ and node u of t such that $t, u \models \varphi$?

Theorem 7 ([14,18,11,22]). *Let \mathcal{L}_0 be the temporal logic over \mathcal{S} with $\mathcal{M} = \emptyset$. The problem TREE-SAT(\mathcal{L}_0) is 2EXPTIME-complete [14,18]. For the intersection free fragment \mathcal{L}_0^- , the problem TREE-SAT(\mathcal{L}_0^-) is EXPTIME-complete [11,22].*

We will extend these results to logics \mathcal{L} and \mathcal{L}^- including MSO modalities. For this, we need the notion of an alternating 2-way tree automaton.

Alternating 2-way Tree Automata An *alternating 2-way tree automaton* (A2A) over $\mathcal{S} = (\Sigma, \Gamma)$ of index $r \in \mathbb{N}$ is a tuple $\mathcal{A} = (Q, \delta, q_0, \text{Acc})$ where Q is a finite set of *states*, $q_0 \in Q$ is the *initial state*, $\text{Acc} : Q \rightarrow \mathbb{N}$ is a *parity acceptance condition* with $r = \max(\text{Acc}(Q))$, and $\delta : Q \times 2^\Sigma \times 2^D \rightarrow \mathcal{B}^+(D \times Q)$ is the transition function where $D = \Gamma \cup \{\text{stay, up}\}$ and $\mathcal{B}^+(D \times Q)$ is the set of positive boolean formulas over $D \times Q$. We only give an intuition of the semantics of A2A and refer to [22,14] for details. An A2A walks in an \mathcal{S} -tree $t = (V, \lambda, \nu)$. A configuration is a set of “threads” (q, u) where $q \in Q$ and $u \in V$ is the current node. For every thread (q, u) , we have to choose some model $\{(d_1, q_1), \dots, (d_n, q_n)\}$ of $\delta(q, \lambda(u), D')$ where D' is the set of directions available at u . Then, we replace (q, u) with n new threads (q_i, u_i) for $1 \leq i \leq n$ where u_i is obtained from u by following direction d_i (if $d_i = \text{stay}$, then $u_i = u$). The parity acceptance condition has to be applied to all infinite paths when we consider the run as a tree, threads (q_i, u_i) being the children of (q, u) . For $u \in V$, a *run* over (t, u) is a run that starts in the single configuration (q_0, u) . The *semantics* $\llbracket \mathcal{A} \rrbracket_t$ contains all nodes u of t such that there is an accepting run of \mathcal{A} over (t, u) .

Theorem 8 ([23]). *Given an A2A \mathcal{A} of index r with n states, one can check in time exponential in $n \cdot r$ if there is a tree t such that $\llbracket \mathcal{A} \rrbracket_t \neq \emptyset$.*

The main ingredient of the proof of Theorem 7 is the construction of an A2A from a given formula, whose existence is given by the following lemma.

Lemma 9 ([14]). *Consider the temporal logic \mathcal{L}_0 over \mathcal{S} with $\mathcal{M} = \emptyset$. For every formula $\varphi \in \text{Form}(\mathcal{L}_0)$, we can construct an A2A \mathcal{B}_φ over \mathcal{S} of exponential size such that, for all \mathcal{S} -trees t , we have $\llbracket \varphi \rrbracket_t = \llbracket \mathcal{B}_\varphi \rrbracket_t$. Moreover, if $\varphi \in \text{Form}(\mathcal{L}_0^-)$ is intersection free, then \mathcal{B}_φ is of polynomial size.*

Using Theorem 8 and Lemma 9, we can extend Theorem 7:

Theorem 10. *Let \mathcal{L} be a temporal logic over \mathcal{S} . The problem TREE-SAT(\mathcal{L}) is 2EXPTIME-complete. For the intersection free fragment \mathcal{L}^- , TREE-SAT(\mathcal{L}^-) is EXPTIME-complete.*

Proof. The lower bounds follow from Theorem 7. We show the upper bounds. Let φ be any \mathcal{L} formula. Let $\text{Subf}(\varphi)$ denote the set of subformulas of φ and let $\text{top}(\xi)$ denote the topmost symbol of $\xi \in \text{Subf}(\varphi)$ which could be \exists or a modality $M \in \mathcal{M} \cup \Sigma \cup \{\neg, \vee\}$: below, we treat atomic propositions $\sigma \in \Sigma$, negation \neg , and disjunction \vee as modalities of arities 0, 1, and 2 resp.

For each modality $M \in \mathcal{M} \cup \Sigma \cup \{\neg, \vee\}$ of arity m , we define an MSO(\mathcal{S}) formula ψ_M with free variables X_0, \dots, X_m by

$$\psi_M(X_0, X_1, \dots, X_m) := \forall x (x \in X_0 \longleftrightarrow \llbracket M \rrbracket(x, X_1, \dots, X_m)).$$

Let $\mathcal{S}_m = (\Sigma \cup \{X_0, \dots, X_m\}, \Gamma)$ so that the node labeling encodes the valuations of the free set variables as usual. By Rabin's theorem, there is a non-deterministic (N1A) tree automaton \mathcal{A}_M recognizing all \mathcal{S}_m -trees satisfying ψ_M . Note that \mathcal{A}_M for $M \in \Sigma \cup \{\neg, \vee\}$ has only one state.

Let $\exists\pi(\xi_1, \dots, \xi_m) \in \text{Subf}(\varphi)$ where ξ_1, \dots, ξ_m are the node formulas checked in path π . Replacing ξ_1, \dots, ξ_m by set variables X_1, \dots, X_m (or new predicates) we will construct using Lemma 9 an A2A $\mathcal{A}_{\exists\pi}$ accepting all \mathcal{S}_m -trees satisfying the "formula"

$$\psi_{\exists\pi}(X_0, X_1, \dots, X_m) := \forall x (x \in X_0 \longleftrightarrow \exists\pi(X_1, \dots, X_m)).$$

By Lemma 9, we can construct automata \mathcal{B}_1 and \mathcal{B}_2 for $\exists\pi(X_1, \dots, X_m)$ and $\neg\exists\pi(X_1, \dots, X_m)$, resp., which are \mathcal{L}_0 formulas. Let ι_1 and ι_2 be the initial states of \mathcal{B}_1 and \mathcal{B}_2 . The automaton $\mathcal{A}_{\exists\pi}$ includes the disjoint union of \mathcal{B}_1 and \mathcal{B}_2 plus a new initial state ι (with even priority in the acceptance condition) and, for $\sigma \subseteq \Sigma \cup \{X_0, \dots, X_m\}$ and $D' \subseteq D$, the transition

$$\delta(\iota, \sigma, D') = \bigwedge_{\gamma \in D'} (\gamma, \iota) \wedge \begin{cases} (\text{stay}, \iota_1) & \text{if } X_0 \in \sigma \\ (\text{stay}, \iota_2) & \text{otherwise.} \end{cases}$$

By Lemma 9, the size of $\mathcal{A}_{\exists\pi}$ is exponential (resp. polynomial) in the size of $\pi(X_1, \dots, X_m)$ (resp. if this path expression is intersection free).

The final automaton \mathcal{A} runs over \mathcal{S}_φ -trees t where $\mathcal{S}_\varphi = (\Sigma \cup \text{Subf}(\varphi), \Gamma)$, i.e., the node labeling includes the (guessed) truth values for $\text{Subf}(\varphi)$. To check that these guesses are correct, \mathcal{A} runs an automaton \mathcal{A}_ξ for each $\xi \in \text{Subf}(\varphi)$.

For each $\xi_0 = M(\xi_1, \dots, \xi_m) \in \text{Subf}(\varphi)$ with $M \in \mathcal{M} \cup \Sigma \cup \{\neg, \vee\}$, we define an automaton \mathcal{A}_{ξ_0} over \mathcal{S}_φ -trees by taking a copy of \mathcal{A}_M which reads a label $\sigma \subseteq \Sigma \cup \text{Subf}(\varphi)$ of t as if it was $\sigma \cap (\Sigma \cup \{\xi_0, \dots, \xi_m\})$ with ξ_i further replaced by X_i . Similarly, for each $\xi_0 = \exists\pi(\xi_1, \dots, \xi_m) \in \text{Subf}(\varphi)$, we define an automaton \mathcal{A}_{ξ_0} over \mathcal{S}_φ -trees by taking a copy of $\mathcal{A}_{\exists\pi}$ which reads a label $\sigma \subseteq \Sigma \cup \text{Subf}(\varphi)$ of t as above.

Finally, \mathcal{A} is the disjoint union of all \mathcal{A}_ξ for $\xi \in \text{Subf}(\varphi)$ together with a new initial state ι which starts all the automata \mathcal{A}_ξ with the initial transitions $\delta(\iota, \sigma, D') = \bigwedge_{\xi \in \text{Subf}(\varphi)} (\text{stay}, \iota_\xi)$ for all $D' \subseteq D$. We can check that an \mathcal{S}_φ -tree $t = (V, \lambda, \nu)$ is accepted by \mathcal{A} iff its projection $t' = (V, \lambda', \nu)$ on Σ is an \mathcal{S} -tree and for each node $u \in V$ we have $\lambda(u) \setminus \Sigma = \{\xi \in \text{Subf}(\varphi) \mid t', u \models \xi\}$. Therefore,

satisfiability of φ over \mathcal{S} -trees is reduced to emptiness of the conjunction of \mathcal{A} with a two state automaton checking that $\varphi \in \lambda(u)$ for some node u of the tree.

The size of \mathcal{A} is at most exponential (resp. polynomial) in the size of φ . Indeed, each \mathcal{A}_ξ with $\text{top}(\xi) \neq \exists$ is of constant size since the MSO modalities are fixed and not part of the input. If $\xi = \exists\pi(\xi_1, \dots, \xi_m)$ then the size of \mathcal{A}_ξ is exponential in $|\pi(X_1, \dots, X_m)|$ (note that ξ_i is replaced by X_i so that its size does not influence the size of \mathcal{A}_ξ). Moreover, if π is intersection free then the size of \mathcal{A}_ξ is polynomial in $|\pi(X_1, \dots, X_m)|$. We deduce from Theorem 8 the 2EXPTIME upper bound for TREE-SAT(\mathcal{L}) and the EXPTIME upper bound for TREE-SAT(\mathcal{L}^-), the intersection free case. \square

Remark 11. Satisfiability for the path-expression free logic CTL considered in Example 2 is known to be EXPTIME-complete [7]. The above procedure gives an EXPTIME procedure for the satisfiability checking meeting the lower bound.

From Ordered Unranked Trees to Binary Trees We recall that an ordered unranked tree can be encoded as a binary tree by removing the edges $(u, v) \in E_{\text{child}}$ whenever v is not a first-child. Note that E_{child} can be retrieved from the binary encoding by the path expression `child` \circ `next*`. Hence any path expression over ordered unranked trees can be converted to a path expression over binary trees (with only a linear blowup in the size), and any MSO(\mathcal{S})-formula over ordered unranked trees can be translated to an MSO(\mathcal{S})-formula over binary trees. Thus, Theorem 10 holds for ordered unranked trees as well:

Problem 12. O-U-TREE-SAT(\mathcal{L}):

INSTANCE: $\varphi \in \text{Form}(\mathcal{L})$

QUESTION: Are there $t \in o.u.\text{Trees}(\mathcal{S})$ and node u of t such that $t, u \models \varphi$?

Theorem 13. *The problem O-U-TREE-SAT(\mathcal{L}) is 2EXPTIME-complete. For the intersection free fragment \mathcal{L}^- , O-U-TREE-SAT(\mathcal{L}^-) is EXPTIME-complete.*

The 2EXPTIME lower bound follows from [18]. The EXPTIME lower bound is inherited from regular XPath [6] (cf. Example 3).

From Nested Traces to Trees Now, we turn to nested traces and we will reduce the satisfiability problems for formulas over nested traces to the satisfiability problems over trees. More precisely, we will transform a given temporal logic over nested traces into some temporal logic over tree encodings of nested traces that “simulates” the original logic. This will allow us to solve the following problem, which is parametrized by *Proc*, *Act*, $k \geq 1$, and a temporal logic \mathcal{L} over the induced signature:

Problem 14. NESTED-TRACE-SAT(\mathcal{L}, k):

INSTANCE: $\varphi \in \text{Form}(\mathcal{L})$

QUESTION: Is there $G \in \text{Traces}_k(\text{Proc}, \text{Act})$ and node u such that $G, u \models \varphi$?

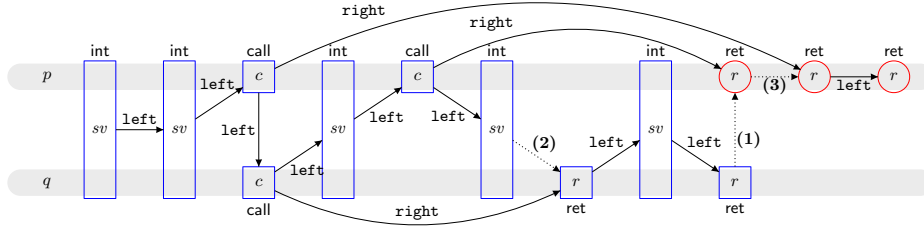


Fig. 4. The tree encoding of a 2-phase linearization

Theorem 15. *The problem $\text{NESTED-TRACE-SAT}(\mathcal{L}, k)$ is in 2EXPTIME . For the intersection free fragment \mathcal{L}^- , $\text{NESTED-TRACE-SAT}(\mathcal{L}^-, k)$ is EXPTIME-complete .*

The proof of Theorem 15 will be developed in the following. In order to exploit Theorem 10, we interpret a k -phase nested trace $G = (V, \lambda, \nu)$ in a (binary) S' -tree (where $S' := (\Sigma \uplus \{1, \dots, k\}, \{\text{left}, \text{right}\})$) using the encoding from [16], extended to infinite trees. Actually, [16] does not consider nested traces but k -phase words. Therefore, we will use linearizations of nested traces. Let $w = (V, \lambda, \leq) \in \text{Lin}_k(G)$. By \leq , we denote the direct successor relation of \leq . Suppose that $V = \{u_0, u_1, u_2, \dots\}$ and that $u_0 < u_1 < u_2 < \dots$ is the corresponding total order. For $0 \leq i < |V|$, we let $\text{phase}_w(u_i) = \min\{j \in \{1, \dots, k\} \mid \lambda(u_0) \dots \lambda(u_i) \text{ is a } j\text{-phase word}\}$. Intuitively, this provides a “tight” factorization of w . We associate with w the S' -tree $t_k^w = (V, \lambda', \nu')$ where the node labeling is given by $\lambda'(u_i) = \lambda(u_i) \cup \{\text{phase}_w(u_i)\}$ and the sets of edges are defined by $E'_{\text{right}} = E_{\text{cr}}$ and $E'_{\text{left}} = \leq \setminus \{(u, v) \in \leq \mid \text{there is } u' \text{ such that } (u', v) \in E_{\text{cr}}\}$. That is, the tree encoding is obtained from the linearization by adding the cr -edges as right children and removing the superfluous linear edges to return nodes having a matching call. Figure 4 depicts the tree t_k^w for the linearization w that was illustrated in Fig. 2. The edges removed from the linearization are shown in dotted lines. The newly added edges are labelled **right**. All \square -nodes are phase 1 and the \circ -nodes are phase 2.

By $\text{Trees}_k(\text{Proc}, \text{Act})$, we denote the set $\{t_k^w \mid w \in \text{Lin}_k(G) \text{ for some } G \in \text{Traces}_k(\text{Proc}, \text{Act})\}$ of *valid* tree encodings. The following was proved in [16] for finite structures, and extends easily to infinite structures.

Lemma 16 ([16]). *There is a formula $\text{TreeEnc}_k \in \text{MSO}(S')$ defining the set $\text{Trees}_k(\text{Proc}, \text{Act})$. Also, there is $\text{less}_k(x, y) \in \text{MSO}(S')$ such that for all k -phase words $w = (V, \lambda, \leq)$ and all $u, v \in V$, we have $u < v$ in w iff $t_k^w \models \text{less}_k(u, v)$.*

Lemma 16 will be used to reduce nested-trace modalities to tree modalities in the proof of Theorem 15. We also need to deal with path expressions, which motivates the following lemma:

Lemma 17. *There exists a path expression $\text{succ}_{\leq k}$ over S' such that, for all k -phase linearizations $w = (V, \lambda, \leq)$, we have $\llbracket \text{succ}_{\leq k} \rrbracket_{t_k^w} = \{(u, v) \in V^2 \mid u < v\}$. In other words, $\text{succ}_{\leq k}$ encodes the successor relation $<$ of \leq in the tree encoding t_k^w of w . Moreover, the length of $\text{succ}_{\leq k}$ is exponential in k .*

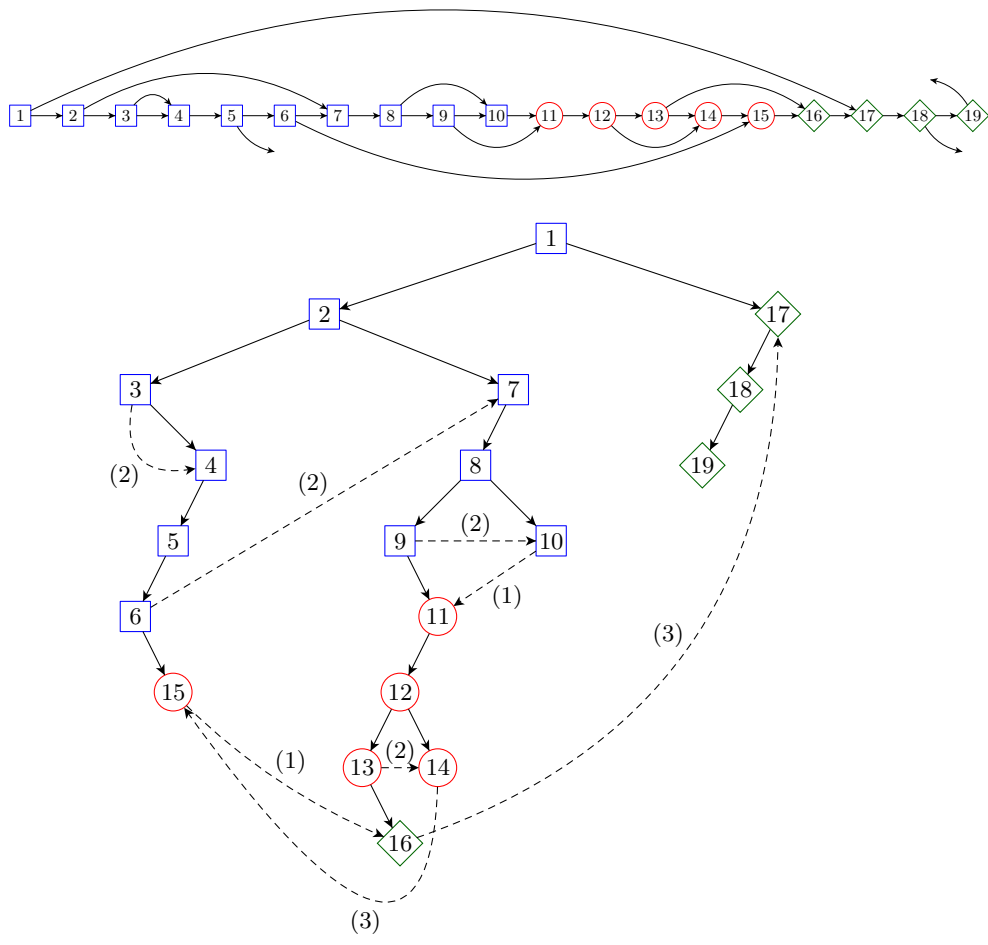


Fig. 5. A 3-phase linearization and the corresponding tree encoding. Nodes in phase 1 are denoted \square , those in phase 2 are denoted \circ and the nodes in phase 3 are denoted \diamond . There are two stacks. The call return edges corresponding to stack 1 are shown above the line and those corresponding to stack 2 are shown below the line. Note that these edges are uniquely determined by the linearization. The dotted edges in the tree are the missing edges from the linearization. The sequence of nodes from the linearization is recovered by traversing the **left**-edges whenever possible, and the dotted edges otherwise. The label on the dotted edge says which case it corresponds to, in the path expressions $\text{succ}_{m-1,m}$ and $\text{succ}_{m,m}$.

Proof. We give the path expression inductively by case analysis. The different cases are illustrated in Figure 5. In the next paragraph we make a few observations which will ease the understanding of the path expressions.

- The phase numbers are monotonically non-decreasing in the linearization. Recall that the tree encoding is obtained from the linearization by adding the **cr**-edges as right children and removing the superfluous linear edges to return nodes having a matching call. Hence the phase numbers are monotonically non-decreasing in any path from the root to any node.
- The **left**-successor of the tree always corresponds to a successor in the linearization.
- If a node has a **right**-child, then it is a call node, and the **right**-child is its corresponding return.
- If $u < v$ in the linearization, then there is a path from node u to node v in the tree which does not visit any node that comes after node v in the linearization. This can be proved by induction on v (with the total order \leq).
- If $u < v$ in the linearization and node v is not the **left**-successor of node u , then node v is a return node which is attached to its call node.
- The first node of any phase greater than 1 is always a return. Moreover it will be attached as a **right**-child if it is a matched return and as a **left**-child otherwise.

We inductively define a path expression $\text{succ}_{\leq m}$ for the successor relation of the linearization restricted to the nodes with phase at most m . That is, $\llbracket \text{succ}_{\leq m} \rrbracket_{t_k^w} = \{(u, v) \in V^2 \mid u < v, \text{phase}_w(u) \leq m, \text{phase}_w(v) \leq m\}$. It will be of the form

$$\text{succ}_{\leq m} = \text{succ}_{\leq m-1} \cup \underset{(a)}{\text{succ}_{m-1,m}} \cup \underset{(b)}{\text{succ}_{m,m}}$$

where cases (a) and (b) are specified below and illustrated in Figures 4 and 5. The base case will be $\text{succ}_{\leq 1} = \text{succ}_{1,1}$.

(a) We will consider the case when u is the last node in phase $m-1$ and v is the first node in phase m . If v is a pending return (that is, it does not have a matching call), we have the path expression $?(m-1) \circ \text{left} \circ ?m$. If v is a matched return, then v is the return of the most recent call with a return in phase m . For example situations, we refer to the edges labelled (1) in Figures 4 and 5. This can be reached by the path expression prev-call-ret_m which goes back to the most recent call which has a return in phase m , and then moves to that return.

$$\text{prev-call-ret}_m = (? \neg \exists (\text{right} \circ ?m) \circ \text{succ}_{\leq m-1}^{-1})^* \circ \text{right} \circ ?m$$

Note that these two cases are mutually exclusive. That is, if a phase starts with a pending return, it is not possible to have a matched return in the same phase with the corresponding call belonging to a smaller phase. Hence we get the path expression from u to v in this case as:

$$\begin{aligned} \text{succ}_{m-1,m} = & ?(m-1) \circ \text{left} \circ ?m \\ & \cup ?(m-1 \wedge \neg \exists \text{succ}_{\leq m-1}) \circ \text{prev-call-ret}_m \end{aligned} \quad (1)$$

(b) If two successive nodes u and v in the linearization are in the same phase m and are connected in the tree by a path where each node is in phase m , then either (i) we can reach v from u by taking a **left** edge, or (ii) v is a return appended to the latest pending call which is also in the phase m and has a return in phase m . In that case, we can reach v by moving up the tree along nodes in phase m until we find the first “pending call” and then taking the **right** edge to node v . For example situations of case (ii), please refer to the edges labelled (2) in Figures 4 and 5. Note that, while moving up the tree, if we move from a right child to its parent, we are at a call node which cannot be a right child. Hence it is not possible to take \mathbf{right}^{-1} twice in succession.

Assume finally that the successive nodes u and v are in the same phase m , but there is no path from u to v visiting only nodes of phase m . This case arises when v is a return whose corresponding call is in a different phase. Now we can move up the tree from u until we see the first node, call it w , belonging to a smaller phase. Clearly w is a call node with its return w' in phase m . Since w' and v are return nodes in the same phase, the corresponding call of v will be before w , and in fact it will be the most recent call before w with its return in phase m . Moving from the parent of w to v is abstracted by the path expression $\mathbf{prev-call-ret}_m$. Examples are edges labelled (3) in Figures 4 and 5.

The path expression $\mathbf{succ}_{m,m}$ is given by:

$$\begin{aligned} \mathbf{succ}_{m,m} = & (?m \circ \mathbf{left} \circ ?m) \cup \\ & ?(m \wedge \neg \exists \mathbf{left}) \circ [(\mathbf{right}^{-1} \cup ?(\neg \exists (\mathbf{right} \circ ?m))) \circ \mathbf{left}^{-1} \circ ?m]^* \circ \\ & [\mathbf{right} \circ ?m \cup \end{aligned} \quad (2)$$

$$\mathbf{right}^{-1} \circ ?(-m) \circ \mathbf{left}^{-1} \circ \mathbf{prev-call-ret}_m] \quad (3)$$

Note that $\mathbf{right}^{-1} \circ \mathbf{left}^{-1}$ allows us to skip call nodes which were previously matched.

All nodes in phase 1 will be connected in the tree, hence we get the basis for the induction, $\mathbf{succ}_{\leq 1} := \mathbf{succ}_{1,1}$ which simplifies to:

$$\begin{aligned} & \mathbf{left} \circ ?1 \cup \\ & ?(1 \wedge \neg \exists \mathbf{left}) \circ [(\mathbf{right}^{-1} \cup ?\neg \exists (\mathbf{right} \circ ?1)) \circ \mathbf{left}^{-1}]^* \circ \mathbf{right} \circ ?1 \end{aligned}$$

Note that the length of the path expression $\mathbf{succ}_{\leq m}$ is exponential in m . \square

We are now ready to prove Theorem 15.

Proof (of Theorem 15). Let $\mathcal{S} = (\Sigma, \Gamma)$ be the signature induced by *Proc* and *Act*, and let $\mathcal{L} = (\mathcal{M}, \mathit{arity}, \llbracket - \rrbracket)$ be the considered temporal logic over nested traces. For $\mathcal{S}' = (\Sigma \uplus \{1, \dots, k\}, \{\mathbf{left}, \mathbf{right}\})$, we define a new temporal logic $\mathcal{L}' = (\mathcal{M}', \mathit{arity}', \llbracket - \rrbracket')$ over \mathcal{S}' -trees and give an inductive, linear-time computable translation T of formulas over \mathcal{L} to “equivalent” formulas over \mathcal{L}' . By “equivalent”, we mean that for all $G \in \mathit{Traces}_k(\mathit{Proc}, \mathit{Act})$ and all k -phase linearizations w of G , we have $\llbracket \varphi \rrbracket_G = \llbracket T(\varphi) \rrbracket_{t_k^w}$ for each node formula φ over \mathcal{L} and $\llbracket \pi \rrbracket_G = \llbracket T(\pi) \rrbracket_{t_k^w}$ for each path formula π over \mathcal{L} .

We set $\mathcal{M}' = \mathcal{M} \cup \{\text{Enc}\}$ where Enc is a new modality with $\text{arity}'(\text{Enc}) = 0$ that characterizes valid tree encodings: the semantics $\llbracket \text{Enc} \rrbracket'$ is given by the formula TreeEnc_k from Lemma 16. We also change the semantics of the modalities from \mathcal{M} : for each $M \in \mathcal{M}$, the new semantics $\llbracket M \rrbracket' \in \text{MSO}(\mathcal{S}')$ is obtained from $\llbracket M \rrbracket \in \text{MSO}(\mathcal{S})$ by replacing each occurrence of $\text{cr}(x, y)$ by $\text{right}(x, y)$ and each occurrence of $\text{succ}_p(x, y)$ by

$$\overline{\text{succ}_p}(x, y) := p(x) \wedge \text{less}_k(x, y) \wedge p(y) \wedge \neg \exists z (\text{less}_k(x, z) \wedge p(z) \wedge \text{less}_k(z, y))$$

where less_k is the formula from Lemma 16. Note that these transformations of the semantics of the modalities only depends on \mathcal{L} and on k (which are not part of the input) and not on the formula for which we want to check satisfiability.

The translation T from formulas over \mathcal{L} to “equivalent” formulas over \mathcal{L}' is defined inductively for node formulas by

$$\begin{aligned} T(\sigma) &= \sigma & T(M(\varphi_1, \dots, \varphi_\ell)) &= M(T(\varphi_1), \dots, T(\varphi_\ell)) \\ T(\neg\varphi) &= \neg T(\varphi) & T(\exists\pi) &= \exists T(\pi) \\ T(\varphi_1 \vee \varphi_2) &= T(\varphi_1) \vee T(\varphi_2) \end{aligned}$$

and for path formulas by

$$\begin{aligned} T(?\varphi) &= ?T(\varphi) & T(\pi_1 \cup \pi_2) &= T(\pi_1) \cup T(\pi_2) \\ T(\text{cr}) &= \text{right} & T(\pi_1 \cap \pi_2) &= T(\pi_1) \cap T(\pi_2) \\ T(\text{cr}^{-1}) &= \text{right}^{-1} & T(\pi_1 \circ \pi_2) &= T(\pi_1) \circ T(\pi_2) \\ T(\text{succ}_p) &= ?p \circ \text{succ}_{\leq k} \circ (? \neg p \circ \text{succ}_{\leq k})^* \circ ?p & T(\pi^*) &= T(\pi)^* \\ T(\text{succ}_p^{-1}) &= ?p \circ \text{succ}_{\leq k}^{-1} \circ (? \neg p \circ \text{succ}_{\leq k}^{-1})^* \circ ?p \end{aligned}$$

where $\text{succ}_{\leq k}$ is defined in Lemma 17. Note that the transformation $T(\pi)$ of a path formula π is linear in $|\pi|$ since k is not part of the input.

Now we check inductively that the translation T is correct. Let $G = (V, \lambda, \nu) \in \text{Traces}_k(\text{Proc}, \text{Act})$, let $w = (V, \lambda, \leq)$ be a k -phase linearization of G and let $t_k^w = (V, \lambda', \nu')$ be the tree encoding of w . We have to show that $\llbracket \varphi \rrbracket_G = \llbracket T(\varphi) \rrbracket_{t_k^w}$ for each node formula φ and $\llbracket \pi \rrbracket_G = \llbracket T(\pi) \rrbracket_{t_k^w}$ for each path formula π .

By definition of t_k^w we have immediately $\llbracket \text{cr} \rrbracket_G = E_{\text{cr}} = E_{\text{right}} = \llbracket \text{right} \rrbracket_{t_k^w}$. The case succ_p is more interesting. We have $(u, v) \in \llbracket \text{succ}_p \rrbracket_G$ iff u is on process p and v is the first node (wrt. the ordering $<$ of w) which is on process p . By Lemma 17, this is described by the formula $T(\text{succ}_p)$ interpreted on the tree encoding t_k^w . The cases cr^{-1} and succ_p^{-1} are similar and the remaining cases for path formulas are obtained directly by induction.

We turn now to node formulas. Again by definition of t_k^w we have immediately $\llbracket \sigma \rrbracket_G = \{u \in V \mid \sigma \in \lambda(u)\} = \{u \in V \mid \sigma \in \lambda'(u)\} = \llbracket \sigma \rrbracket_{t_k^w}$ for each $\sigma \in \Sigma$. The cases $\neg\varphi$, $\varphi_1 \vee \varphi_2$, and $\exists\pi$ follow directly by induction. It remains to deal with a modality M of arity ℓ . We prove by induction on the MSO formula $\llbracket M \rrbracket$ that for all $U_1, \dots, U_\ell \subseteq V$ and all nodes $u \in V$, we have $G \models \llbracket M \rrbracket(u, U_1, \dots, U_\ell)$ iff $t_k^w \models \llbracket M \rrbracket'(u, U_1, \dots, U_\ell)$. Among the atomic subformulas, the only non trivial case is

for $\text{succ}_p(x, y)$ and it follows from Lemma 16 and the definition of $\overline{\text{succ}_p}(x, y)$ given above. The non atomic cases follow directly by induction.

Finally, a formula $\varphi \in \text{Form}(\mathcal{L})$ is satisfiable over k -phase nested traces iff the formula $\text{Enc} \wedge T(\varphi) \in \text{Form}(\mathcal{L}')$ is satisfiable over S' -trees. Using Theorem 10 we get the upper bounds stated in Theorem 15. The lower bound for the intersection free fragment is proved in the following proposition. \square

Proposition 18. *Let Proc and Act be non-empty finite sets inducing \mathcal{S} , let $k \geq 1$, and let \mathcal{L}^- be an intersection free temporal logic over \mathcal{S} . The problem NESTED-TRACE-SAT(\mathcal{L}^-, k) is EXPTIME-hard.*

Proof. We show a reduction from the EXPTIME complete logic NWTL [1] to the temporal logic \mathcal{L}_0^- with no modalities and only one process p . The modalities are $X, Y, X^{\text{cr}}, Y^{\text{cr}}, U^s$ and S^s which are the same as $\text{EX}, \text{EY}, X^{\text{cr}}, Y^{\text{cr}}, \text{EU}^s$ and ES^s of the temporal logic NTrLTL defined in Example 5 when $|\text{Proc}| = 1$. Below we give its semantics through path expressions. This also evinces the translation of an NWTL formula φ into an equivalent \mathcal{L}_0^- -formula $\overline{\varphi}$ (by induction).

$$\begin{aligned} \overline{X\varphi} &= \exists(\text{succ}_p \circ \overline{\varphi}) & \overline{Y\varphi} &= \exists(\text{succ}_p^{-1} \circ \overline{\varphi}) \\ \overline{X^{\text{cr}}\varphi} &= \exists(\text{cr} \circ \overline{\varphi}) & \overline{\varphi U^s \psi} &= \exists((\overline{\varphi} \circ (\text{succ}_p \cup \text{cr}))^* \circ \overline{\psi}) \\ \overline{Y^{\text{cr}}\varphi} &= \exists(\text{cr}^{-1} \circ \overline{\varphi}) & \overline{\varphi S^s \psi} &= \exists((\overline{\varphi} \circ (\text{succ}_p^{-1} \cup \text{cr}^{-1}))^* \circ \overline{\psi}) \quad \square \end{aligned}$$

Remark 19. If k is given as part of the input, the above method for modalities does not work: the new semantics $\llbracket M \rrbracket'$ over trees depend on k and are no more fixed and independent of the input. However, if we consider the fragment \mathcal{L}_0 with no MSO modalities, we get a 3EXPTIME procedure even if k is part of the input since the length of the path expression $T(\pi)$ is linear in $|\pi|$ and exponential in k . Moreover, for the intersection free fragment \mathcal{L}_0^- , we get a 2EXPTIME procedure.

5 Model Checking

Our approach extends to model checking. We can define a model of concurrent recursive programs, called concurrent recursive Kripke structures (CRK), that generates nested traces. It is similar to the concurrent visibly pushdown automata from [5].

Definition 20. *A concurrent recursive Kripke structure (CRK) over finite sets Proc and Act is a tuple $\mathcal{K} = ((S_p)_{p \in \text{Proc}}, \Delta, \iota)$. The S_p are disjoint finite sets of local states (S_p containing the local states of process p). Given a set $P \subseteq \text{Proc}$, we let $S_P := \prod_{p \in P} S_p$. The tuple $\iota \in S_{\text{Proc}}$ is a global initial state. Finally, Δ provides the transitions, which are divided into four sets: $\Delta = (\Delta_{\text{call}}, \Delta_{\text{ret}}^1, \Delta_{\text{ret}}^2, \Delta_{\text{int}})$ where*

- $\Delta_{\text{call}} \subseteq \bigcup_{p \in \text{Proc}} (S_p \times \text{Act} \times S_p)$,
- $\Delta_{\text{ret}}^1 \subseteq \bigcup_{p \in \text{Proc}} (S_p \times \text{Act} \times S_p)$,

- $\Delta_{ret}^2 \subseteq \bigcup_{p \in Proc} (S_p \times S_p \times Act \times S_p)$, and
- $\Delta_{int} \subseteq \bigcup_{p \in Proc} (S_p \times Act \times S_p)$.

Let $\mathcal{S} = \bigcup_{p \in Proc} S_p$. For $s \in \mathcal{S}$ and $p \in Proc$, we let s_p be the p -th component of s (if it exists). A run of a CRK \mathcal{K} is an \mathcal{S}' -graph $G = (V, \lambda, \nu)$ where $\mathcal{S}' = (\Sigma \uplus \biguplus_{p \in Proc} S_p, \Gamma)$ with $\Sigma = Proc \cup Act \cup Type$ and $\Gamma = \{cr\} \cup \{succ_p \mid p \in Proc\}$, and the following conditions hold:

- The graph G without the labeling from $\bigcup_{p \in Proc} S_p$ is a nested trace. That is $nt(G) := (V, \lambda', \nu)$ where $\lambda'(u) = \lambda(u) \cap \Sigma$ is a nested trace over $Proc$ and Act .
- Every node u is labeled with one, and only one, state from S_p for each process $p \in Proc(u)$. This state is denoted $\rho(u)_p$. The label of a node u does not contain any state from S_p if $p \notin Proc(u)$. That is, for all $p \in Proc$ and all $u \in V$,

$$\lambda(u) \cap S_p = \begin{cases} \{\rho(u)_p\} & \text{if } p \in \lambda(u) \\ \emptyset & \text{otherwise.} \end{cases}$$

This defines a mapping $\rho : V \rightarrow \mathcal{S}$ by $\rho(u) = (\rho(u)_p)_{p \in Proc(u)}$.

- Let us determine another mapping $\rho^- : V \rightarrow \mathcal{S}$ as follows: for $u \in V$, we let $\rho^-(u) = (\rho^-(u)_p)_{p \in Proc(u)}$ where $\rho^-(u)_p = \rho(u')_p$ if $(u', u) \in E_{succ_p}$, and $\rho^-(u)_p = \iota_p$ if there is no u' such that $(u', u) \in E_{succ_p}$. The following hold, for every $u, u' \in V$ and $a \in Act$:

- $(\rho^-(u), a, \rho(u)) \in \Delta_{call}$ if $u \in V_{call} \cap V_a$
- $(\rho^-(u), a, \rho(u)) \in \Delta_{ret}^1$ if $u \in V_{ret} \cap V_a$ and there is no v with $(v, u) \in E_{cr}$
- $(\rho(u'), \rho^-(u), a, \rho(u)) \in \Delta_{ret}^2$ if $u \in V_{ret} \cap V_a$ and $(u', u) \in E_{cr}$
- $(\rho^-(u), a, \rho(u)) \in \Delta_{int}$ if $u \in V_{int} \cap V_a$

We are only interested in maximal runs. We say that a run G of a CRK \mathcal{K} is *maximal* if G is not a strict prefix of another run of \mathcal{K} . The *language* $L(\mathcal{K})$ of \mathcal{K} is the set $\{nt(G) \mid G \text{ is a maximal run of } \mathcal{K}\}$. By $L_k(\mathcal{K})$, we denote its restriction $L(\mathcal{K}) \cap Traces_k(Proc, Act)$ to k -phase nested traces.

Let $Proc$ and Act be non-empty finite sets inducing signature \mathcal{S} , let $k \geq 1$, and let \mathcal{L} be a temporal logic over \mathcal{S} . We are interested in the following decision problem.

Problem 21. MODEL-CHECKING(\mathcal{L}, k):

INSTANCE: CRK \mathcal{K} and $\varphi \in \text{Form}(\mathcal{L})$

QUESTION: Do we have $\mathcal{K} \models_k \varphi$, i.e.,

for all $G \in L_k(\mathcal{K})$, is there a node u of G such that $G, u \models \varphi$?

We show the following result:

Theorem 22. *The problem MODEL-CHECKING(\mathcal{L}, k) is in 2EXPTIME. For the intersection free fragment \mathcal{L}^- , MODEL-CHECKING(\mathcal{L}^-, k) is EXPTIME.*

Proof. We will reduce the model-checking problem to the satisfiability problem by encoding maximal runs of a CRK \mathcal{K} with a formula MAXRUN. For this, we enrich \mathcal{L} to \mathcal{L}' with the additional unary modality EN (there exists a node) whose semantics is defined by $\llbracket \text{EN} \rrbracket(x, X) = \exists y (y \in X)$.

Now we will describe the maximal runs of the CRK \mathcal{K} by the formula MAXRUN = VAL \wedge MAX \wedge \neg EN \neg TRANS. Here, VAL says that the labeling by states is valid. That is, no node is labelled by two states of the same process, and a node is labeled p iff it is labeled by some state from S_p .

$$\text{VAL} = \neg \bigvee_{p \in \text{Proc}} \text{EN} \left(\neg \left(p \longleftrightarrow \bigvee_{s \in S_p} s \right) \vee \bigvee_{s_1 \neq s_2 \in S_p} (s_1 \wedge s_2) \right)$$

Formula MAX says that the maximal nodes of the nested run do not enable any transition. Formula TRANS says that the labeling of the current node and its predecessors comply with the transition relations.

For $s \in \mathcal{S}$ we let $\Delta_{\text{int}}(s) = \{(a, s') \in \text{Act} \times \mathcal{S} \mid (s, a, s') \in \Delta_{\text{int}}\}$ and we define similarly $\Delta_{\text{call}}(s)$, $\Delta_{\text{ret}}(s)$, and $\Delta_{\text{ret}}(s_1, s_2)$. In the following, for all $s \in \mathcal{S}$ and $p \in \text{Proc}$, let “ $s_p = \iota_p$ ” be a shorthand for **true** (\top) if $s_p = \iota_p$ and **false** (\perp) otherwise. Then, we define MAX = MAX_{call} \wedge MAX_{int} \wedge MAX_{ret}¹ \wedge MAX_{ret}² by

$$\begin{aligned} \text{MAX}_{\text{call}} &= \neg \bigvee_{p \in \text{Proc} \mid s_p \in S_p \mid \Delta_{\text{call}}(s_p) \neq \emptyset} (“s_p = \iota_p” \wedge \neg \text{EN } p) \vee \text{EN}(s_p \wedge p \wedge \neg \exists \text{succ}_p) \\ \text{MAX}_{\text{int}} &= \neg \bigvee_{P \subseteq \text{Proc} \mid s \in S_P \mid \Delta_{\text{int}}(s) \neq \emptyset} \bigwedge_{p \in P} (“s_p = \iota_p” \wedge \neg \text{EN } p) \vee \text{EN}(s_p \wedge p \wedge \neg \exists \text{succ}_p) \\ \text{MAX}_{\text{ret}}^1 &= \neg \bigvee_{p \in \text{Proc} \mid s_p \in S_p \mid \Delta_{\text{ret}}(s_p) \neq \emptyset} (“s_p = \iota_p” \wedge \neg \text{EN } p) \vee \\ &\quad (\text{EN}(s_p \wedge p \wedge \neg \exists \text{succ}_p) \wedge \neg \text{EN}(p \wedge \text{call} \wedge \neg \exists \text{cr})) \\ \text{MAX}_{\text{ret}}^2 &= \neg \bigvee_{p \in \text{Proc} \mid s_1, s_2 \in S_p \mid \Delta_{\text{ret}}(s_1, s_2) \neq \emptyset} \text{EN}[s_1 \wedge p \wedge \text{call} \wedge \neg \exists \text{cr} \wedge \neg \exists (\text{succ}_p \circ \text{succ}_p^* \circ ?(\text{call} \wedge \neg \exists \text{cr}))] \\ &\quad \wedge \text{EN}(s_2 \wedge p \wedge \neg \exists \text{succ}_p) \end{aligned}$$

Then, we define TRANS = TRANS_{call} \vee TRANS_{ret}¹ \vee TRANS_{ret}² \vee TRANS_{int}.

$$\begin{aligned} \text{TRANS}_{\text{call}} &= \bigvee_{\substack{p \in \text{Proc} \\ (s, a, s') \in \Delta_{\text{call}}}} \text{call} \wedge a \wedge p \wedge s' \wedge \\ &\quad ((“s = \iota_p” \wedge \neg \exists \text{succ}_p^{-1}) \vee \exists (\text{succ}_p^{-1} \circ ?s)) \\ \text{TRANS}_{\text{ret}}^1 &= \bigvee_{\substack{p \in \text{Proc} \\ (s, a, s') \in \Delta_{\text{ret}}^1}} \text{ret} \wedge a \wedge p \wedge s' \wedge \neg \exists \text{cr}^{-1} \wedge \\ &\quad ((“s = \iota_p” \wedge \neg \exists \text{succ}_p^{-1}) \vee \exists (\text{succ}_p^{-1} \circ ?s)) \\ \text{TRANS}_{\text{ret}}^2 &= \bigvee_{\substack{p \in \text{Proc} \\ (s, s', a, s'') \in \Delta_{\text{ret}}^2}} \text{ret} \wedge a \wedge p \wedge s'' \wedge \exists (\text{succ}_p^{-1} \circ ?s') \wedge \exists (\text{cr}^{-1} \circ ?s) \\ \text{TRANS}_{\text{int}} &= \bigvee_{\substack{P \subseteq \text{Proc}, s, s' \in S_P \\ (s, a, s') \in \Delta_{\text{int}}}} \text{int} \wedge a \wedge \bigwedge_{p \notin P} \neg p \wedge \bigwedge_{p \in P} [p \wedge s'_p \wedge \\ &\quad ((“s_p = \iota_p” \wedge \neg \exists \text{succ}_p^{-1}) \vee \exists (\text{succ}_p^{-1} \circ ?s_p))] \end{aligned}$$

Note that the sizes of MAX and TRANS are linear in the size of the CRK \mathcal{K} . Moreover, a nested trace decorated with states satisfies MAXRUN iff it defines a maximal run of the CRK \mathcal{K} . Thus, $\mathcal{K} \models_k \varphi$ iff the formula $\text{MAXRUN} \wedge \neg \text{EN} \varphi$ is not satisfiable by a (state-labeled) k -phase nested trace. This concludes the reduction. \square

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